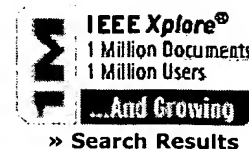




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Instrumentation & Measurement Magazine, IEEE , Volume: 4 , Issue: 1 , March 2001

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2 Table-based dynamic FET model assembled from small-signal models

Ce-Jun Wei; Tkachenko, Y.A.; Bartle, D.;

Microwave Theory and Techniques, IEEE Transactions on , Volume: 47 , Issue: 6 , June 1999

Pages:700 - 705

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3 A simplified hybrid method for calculating the frequency-dependent inductances of transmission lines with rectangular cross section

Shuzhou Fang; Xiabo Tang; Zeyi Wang; Xianlong Hong;

Design Automation Conference, 2000. Proceedings of the ASP-DAC 2000. Asia and South Pacific , 25-28 Jan. 2000

Pages:453 - 456

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4 Table-based FET model assembled from small-signal models

Wei, C.J.; Tkachenko, Y.A.; Bartle, D.;

Radio and Wireless Conference, 1998. RAWCON 98. 1998 IEEE , 9-12 Aug. 1998

Pages:355 - 358

[\[Abstract\]](#) [\[PDF Full-Text \(252 KB\)\]](#) IEEE CNF

5 An efficient constant envelope n/4 QPSK modulation scheme

Tong-Fu Lee; Shih-Ho Wang; Chia-Liang Liu;

Circuits and Systems, 1997. Proceedings of the 40th Midwest Symposium on , Volume: 1 , 3-6 Aug. 1997
Pages:623 - 625 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(172 KB\)\]](#) [IEEE CNF](#)

6 **A digital convergence system using cubic splines**

Chae Gon Oh; Yunmo Chung; Chang Wan Hong; Sung Pyo Hong;

Consumer Electronics, IEEE Transactions on , Volume: 45 , Issue: 4 , Nov 1999
Pages:1247 - 1252

[\[Abstract\]](#) [\[PDF Full-Text \(172 KB\)\]](#) [IEEE JNL](#)

7 **Efficient two-dimensional interpolation scheme suitable for nonlinear microwave device modelling**

Rizzoli, V.; Cecchetti, C.; Costanzo, A.;

Electronics Letters , Volume: 31 , Issue: 19 , 14 Sept. 1995
Pages:1674 - 1675

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) [IEEE JNL](#)

8 **A digital convergence system using cubic splines**

Chae Gon Oh; Yunmo Chung; Chang Wan Hong;

VLSI and CAD, 1999. ICVC '99. 6th International Conference on , 26-27 Oct. 1999
Pages:593 - 595

[\[Abstract\]](#) [\[PDF Full-Text \(184 KB\)\]](#) [IEEE CNF](#)

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Shenai, K.;

Power Electronics, IEEE Transactions on , Volume: 6 , Issue: 3 , July 1991

Pages:539 - 547

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2 A SOS MOSFET SPICE model for confident analogue circuit design

Howes, R.; Redman-White, W.; Nichols, K.G.; Robinson, M.; Kerr, J.; Mole, P.J.;

SOI Conference, 1991. Proceedings., 1991 IEEE International , 1-3 Oct. 1991

Pages:90 - 91

[\[Abstract\]](#) [\[PDF Full-Text \(116 KB\)\]](#) IEEE CNF

3 A computer simulation model for simulating distortion in FET resistors

Scheinberg, N.; Pinkhasov, A.;

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 19 , Issue: 9 , Sept. 2000

Pages:981 - 989

[\[Abstract\]](#) [\[PDF Full-Text \(232 KB\)\]](#) IEEE JNL

4 Fluctuations of the low frequency noise of MOS transistors and their modeling in analog and RF-circuits

Brederlow, R.; Weber, W.; Schmitt-Landsiedel, D.; Thewes, R.;

Electron Devices Meeting, 1999. IEDM Technical Digest. International , 5-8 Dec. 1999

Pages:159 - 162

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) IEEE CNF

5 Simple nonlinear large signal MOSFET model parameter extraction for class E amplifiers

Molina Gaudo, P.; del Aguila Lopez, F.; Schonwalder, P.P.; Artigas, J.N.;

Electronics, Circuits and Systems, 2002. 9th International Conference on , Volume: 1 , 15-18 Sept. 2002
Pages:269 - 272 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(291 KB\)\]](#) IEEE CNF

6 Synchronous rectifier as a test circuit for high-frequency power MOSFET model verification

Fischer, K.; Shenai, K.;

Industry Applications Conference, 1995. Thirtieth IAS Annual Meeting, IAS '95., Conference Record of the 1995 IEEE , Volume: 2 , 8-12 Oct. 1995

Pages:1091 - 1097 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(384 KB\)\]](#) IEEE CNF

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